

# UNITED STA DEPARTMENT OF COMMERCE Patent and Trademark Office

Address: COMMISSIONER OF PATENTS AND TRADEMARKS Washington, D.C. 20231

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	A	ATTORNEY DOCKET NO.	
<del></del>			EXAMINER		
			ART UNIT	PAPER NUMBER	
			DATE MAILED:	3	

Please find below and/or attached an Office communication concerning this application or proceeding.

**Commissioner of Patents and Trademarks** 

· · · · · · · · · · · · · · · · · · ·		
•	Application No.	Applicant(s)
Al. At E All E title .	09/911,495	BARENBURG ET AL
Notice of Allowability	Examiner	Art Unit
	   Scott Gever	2813
The MAILING DATE of this communication at All claims being allowable, PROSECUTION ON THE MERITS herewith (or previously mailed), a Notice of Allowance (PTOLNOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT of the Office or upon petition by the applicant. See 37 CFR 1.3.  1.	Examiner  Scott Geyer  Ppears on the cover sheet was Solven (COR REMAINS) CLOSED in the supplication is 313 and MPEP 1308.  Examiner.  Index 35 U.S.C. § 119(a)-(d) of the second share been received in Application documents have been received in Application has been received in the supplication in the supplication. This This is application. This This is application. This This is application. This This is application. The supplication is a supplication in the supplication in the supplication is a supplication	ith the correspondence address in this application. If not included function will be mailed in due course. THIS subject to withdrawal from issue at the initiative or (f).  on No ed in this national stage application from the first a provisional application ed.  /or 121.  a reply complying with the requirements noted REE-MONTH PERIOD IS NOT EXTENDABLE  KAMINER'S AMENDMENT or NOTICE Of a aration is deficient.  ew ( PTO-948) attached  ich has been approved by the Examiner.
(c) including changes required by the attached Examination including such as the application number (see 37 CF of each sheet. The drawings should be filed as a separate page of the control of the contr	FR 1.84(c)) should be written on	the drawings in the top margin (not the back)
9. DEPOSIT OF and/or INFORMATION about the deattached Examiner's comment regarding REQUIREMENT FO	eposit of BIOLOGICAL MAT OR THE DEPOSIT OF BIOLOG	FERIAL must be submitted. Note the SICAL MATERIAL.
Attachment(s)		
<ul> <li>Notice of References Cited (PTO-892)</li> <li>Notice of Draftperson's Patent Drawing Review (PTO-948)</li> <li>Information Disclosure Statements (PTO-1449), Paper No.</li> <li>Examiner's Comment Regarding Requirement for Deposition of Biological Material</li> </ul>	3) 4 ☐ Intervie o. <u>2</u> . 6 ☐ Examir	of Informal Patent Application (PTO-152)  ew Summary (PTO-413). Paper No  ner's Amendment/Comment  ner's Statement of Reasons for Allowance

Page 2

'Application/Control Number: 09/911,495

Art Unit: 2813

#### **DETAILED ACTION**

#### Drawings

1. The drawings as submitted on 25 July 2001 are acceptable.

# Specification

2. The specification and abstract as submitted are acceptable.

## Allowable Subject Matter

- 3. Claims 1-24 are allowed.
- 4. The following is an examiner's statement of reasons for allowance: applicant's independent device claim 1 and independent method claim 13 recite a device and method for making such device, respectively, with a specific order of layers.

The device is constructed of the following: a mono-crystalline substrate, a layer of amorphous oxide, a layer of mono-crystalline perovskite oxide material, a mono-crystalline compound semiconductor material and an arrayed wavelength grating device.

The method for making the device is as follows: providing a mono-crystalline substrate, depositing a layer of mono-crystalline perovskite oxide material on the silicon substrate, forming an amorphous oxide layer between the perovskite layer and the silicon substrate, forming a mono-crystalline compound semiconductor layer over the perovskite oxide layer and forming an arrayed wavelength grating device over the mono-crystalline substrate.

Droopad et al. (US 2001/0013313 A1) teach a method and apparatus for growing mono-crystalline layers on a silicon wafer (paragraph 0004). Also, multiple mono-

'Application/Control Number: 09/911,495

Art Unit: 2813

crystalline layers can be grown on top of one another (paragraph 0005). Perovskite oxides can be layered as a buffer layer (column 0020).

Cheung et al. (6,071,795) teach a layered semiconductor device which can use a perovskite compound as a second substrate (see claims 1 and 15).

McKee et al. (5,876,866) teach a multi-layered semiconductor device structure and process for making the structure. The structure includes a perovskite oxide layer (column 1, lines 65 et seq., continuing to column 2, lines 1-3).

Baeuerle (4,63,826) teaches perovskite oxides as dielectric materials in the ceramic and single crystal form and used in electronics such as capacitors and piezo electric elements (column 1, lines 60 et seq.).

Japanese Patent (JP 01052329, to Sumitomo Electric Ind. Co.) teach an amorphous oxide layer deposited onto a metal substrate. An oxide super conducting layer containing perovskite is deposited on the amorphous oxide layer.

However, none of the above references, or any combination of the above references, specifically teach or suggest all layers or process of making those layers as recited in applicant's independent claims 1 and 13. Also, none of the references teach an arrayed wavelength grating device. Thus, all claims dependent on claims 1 and 13 are also allowable.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Page 4

'Application/Control Number: 09/911,495

Art Unit: 2813

### Conclusion

**5.** Any inquiry concerning this communication or earlier communications from the examiner should be directed to Scott Geyer whose telephone number is (703)306-5866. The examiner can normally be reached on weekdays, between 9:00am - 5:30pm. The examiner may also be reached via e-mail: **scott.geyer@uspto.gov**.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Charles L. Bowers can be reached on (703)308-2417. The fax phone numbers for the organization where this application or proceeding is assigned are (703)305-0194 for regular communications and (703)305-0194 for After Final communications.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is (703)308-0956.

sbg October 31, 2001

- -



# UNITED STATE DEPARTMENT OF COMMERCE Patent and Trademark Office

#### NOTICE OF ALLOWANCE AND ISSUE FEE DUE

APPLICATION NO.	FILING DATE	TOTAL CLAIMS	EXAM	EXAMINER AND GROUP ART UNIT		DATE MAILED
			-		*	* #
First Named Applicant	<u> </u>			· • ·		
TLE OF IVENTION				γ.		
ATTY'S DOCKET NO.	CLASS-SUBCLASS	BATCH NO.	APPLN. TYPE	SMALL ENTITY	FEE DUE	DATE DUE

THE APPLICATION IDENTIFIED ABOVE HAS BEEN EXAMINED AND IS ALLOWED FOR ISSUANCE AS A PATENT. PROSECUTION ON THE MERITS IS CLOSED.

THE ISSUE FEE MUST BE PAID WITHIN <u>THREE MONTHS</u> FROM THE MAILING DATE OF THIS NOTICE OR THIS APPLICATION SHALL BE REGARDED AS ABANDONED. THIS STATUTORY PERIOD CANNOT BE EXTENDED.

#### HOW TO RESPOND TO THIS NOTICE:

- I. Review the SMALL ENTITY status shown above. If the SMALL ENTITY is shown as YES, verify your current SMALL ENTITY status:
  - A. If the status is changed, pay twice the amount of the FEE DUE shown above and notify the Patent and Trademark Office of the change in status, or
  - B. If the status is the same, pay the FEE DUE shown above.

If the SMALL ENTITY is shown as NO:

- A. Pay FEE DUE shown above, or
- B. File verified statement of Small Entity Status before, or with, payment of 1/2 the FEE DUE shown above.
- II. Part B-Issue Fee Transmittal should be completed and returned to the Patent and Trademark Office (PTO) with your ISSUE FEE. Even if the ISSUE FEE has already been paid by charge to deposit account, Part B Issue Fee Transmittal should be completed and returned. If you are charging the ISSUE FEE to your deposit account, section "4b" of Part B-Issue Fee Transmittal should be completed and an extra copy of the form should be submitted.
- III. All communications regarding this application must give application number and batch number.

  Please direct all communications prior to issuance to Box ISSUE FEE unless advised to the contrary.

IMPORTANT REMINDER: Utility patents issuing on applications filed on or after Dec. 12, 1980 may require payment of maintenance fees. It is patentee's responsibility to ensure timely payment of maintenance fees when due.

PATENT AND TRADEMARK OFFICE COPY